

# CD50/CD50H SERIES

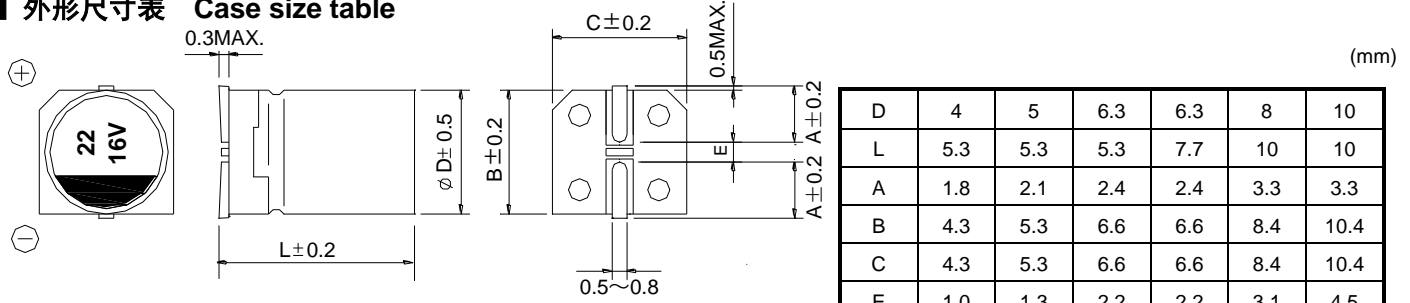


- CD50: +85°C, 2000 小时 Load life of 2000 hours at +85°C
- CD50H: +105°C, 2000 小时 Load life of 2000 hours at +105°C
- 无引线, 片状结构 No lead wires, chip type structure
- 适用于自动表面贴装技术和高密度电路 Suitable for automatic SMT and high density circuits.

## 主要技术性能 Specifications

项目 Item	特性 Characteristics																
使用温度范围 Operating temperature range(°C)	-40~+85 (CD50H: -55~+105)																
额定电压范围 Rated voltage range(V)	4~50																
标称电容容量范围 Nominal capacitance range(μ F)	0.1~1000																
标称电容容量允许偏差 Capacitance tolerance(%)	±20 (20°C,120Hz)																
漏电流 Leakage current(μ A)	$I \leq 0.01C_R U_R$ 或 or 3 (取较大值 whichever is greater)																
损耗角正切值 Dissipation factor(tg δ)	<table border="1"> <thead> <tr> <th><math>U_R</math> (V)</th> <th>4</th> <th>6.3</th> <th>10</th> <th>16</th> <th>25</th> <th>35</th> <th>50</th> </tr> </thead> <tbody> <tr> <td>tg δ(max.)</td> <td>0.37</td> <td>0.28</td> <td>0.24</td> <td>0.20</td> <td>0.16</td> <td>0.13</td> <td>0.12</td> </tr> </tbody> </table>	$U_R$ (V)	4	6.3	10	16	25	35	50	tg δ(max.)	0.37	0.28	0.24	0.20	0.16	0.13	0.12
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## 外形尺寸表 Case size table



## 标称电容量、额定电压与外形尺寸对应表 Nominal capacitance, rated voltage and case size table

$C_R$ (μ F)	$U_R$ (V) φD×L(mm)	4 (0G)	6.3 (0J)	10 (1A)	16 (1C)	25 (1E)	35 (1V)	50 (1H)
		0.1	(0R1)					
0.22	(R22)							φ4×5.3
0.33	(R33)							φ4×5.3
0.47	(R47)							φ4×5.3
1	(010)							φ4×5.3
2.2	(2R2)							φ4×5.3
3.3	(3R3)							φ4×5.3
4.7	(4R7)					φ4×5.3	φ4×5.3	φ5×5.3
10	(100)				φ4×5.3	φ5×5.3	φ5×5.3	φ6.3×5.3
22	(220)	φ4×5.3	φ4×5.3	φ5×5.3	φ5×5.3	φ6.3×5.3	φ6.3×5.3	φ6.3×5.3
33	(330)	φ5×5.3	φ5×5.3	φ5×5.3	φ6.3×5.3	φ6.3×5.3	φ6.3×5.3	φ6.3×7.7
47	(470)	φ5×5.3	φ5×5.3	φ6.3×5.3	φ6.3×5.3	φ6.3×5.3	φ6.3×5.3	φ6.3×7.7
100	(101)	φ6.3×5.3	φ6.3×5.3	φ6.3×5.3	φ6.3×5.3	φ6.3×7.7	φ6.3×7.7	φ8×10
220	(221)	φ6.3×5.3	φ6.3×7.7	φ6.3×7.7	φ6.3×7.7	φ8×10	φ10×10	
330	(331)	φ6.3×7.7	φ6.3×7.7	φ8×10	φ8×10	φ8×10		
470	(471)	φ6.3×7.7	φ8×10	φ8×10	φ10×10			
1000	(102)		φ10×10					